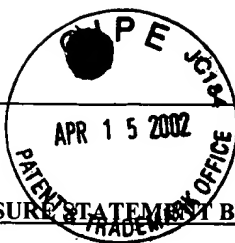


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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

APPLICANT(S): Heegard et al.

FILING DATE: Aug. 4, 1999

GROUP:

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
SA	AA	4,495,619	Jan. 22, 1985	Acampora	380	104	02/13/94
SA	AB	5,204,876	Apr. 20, 1993	Bruckert et al.	319	180	05/13/91
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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANS- LATION YES NO
SA	AD	0 407 673	Jan. 16, 1991	EPO	H04L	27/34	✓

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

SA	AE	Jain, Y., "Convolutional codes improve bit-error rate in digital systems," <u>EDN Electrical Design News</u> , Cahners Publishing Co., vol. 35, no. 17, August 20, 1990, pp. 129-134.

EXAMINER

Esau Abraham

DATE CONSIDERED

03/12/03

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.